

Serial No. 10/085,086
Art Unit 2638

IN THE CLAIMS

Claim 1-12. (withdrawn)

Claim 13. (currently amended) A transparent data path architecture for an optical-electrical-optical (OEO) switch comprising:

means to recover a data rate from an incoming serial signal;

means to monitor signal quality of the incoming signal using an ingress CDR to monitor data eye opening and using a Performance Monitoring Module to monitor layer 1 overhead of the incoming signal;

means to provide data integrity across the transparent switching fabric; and

~~The transparent data architecture as defined in claim 1~~ having means to provide line loopback of a signal having undergone re-shaping, re-amplification and re-shaping in a clock data recovery unit.

Claim 14. (currently amended) A transparent data path architecture for an optical-electrical-optical (OEO) switch comprising:

means to recover a data rate from an incoming serial signal;

means to monitor signal quality of the incoming signal using an ingress CDR to monitor data eye opening and using a Performance Monitoring Module to monitor layer 1 overhead of the incoming signal;

means to provide data integrity across the transparent switching fabric; and

~~The transparent data architecture as defined in claim 1~~ having means to provide loopback functionality wherein a signal is looped back through either the an active switching fabric plane or ~~the~~ a back-up switching fabric plane.

Claims 15-18. (withdrawn)